

Curriculum Vitae



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PROFESSIONAL POSITIONS

University of Texas, Austin

Professor in Electrical and Computer Engineering
Temple Foundation Endowed Professorship #3 (08/2021 -)
Professor in Chemistry
Fellow of the Dow Professorship (08/2021 -)

University of Illinois

Interim Director of Nick Holonyak Jr. Micro and Nanotechnology Laboratory (01/2020 – 08/2021)
Donald Bigger Willett Professor in Engineering (03/2020- 08/2021)
Professor in Electrical and Computer Engineering (2015- 2020)
Willet Faculty Scholar, College of Engineering (2015-2020)
Associate Professor in Electrical and Computer Engineering (2012-2015)
Assistant Professor in Electrical and Computer Engineering (2007-2012)
Faculty Affiliate, Mechanical Science and Engineering (2011-)
Research Faculty, Materials Research Laboratory (2010-)
Faculty Affiliate, Beckman Institute (2007-)
Faculty Affiliate, Materials Science and Engineering (2007-)

EpiWorks Inc.

R&D Manager (2003-2007)
Senior Engineer (2001-2003)

University of Illinois

Research Assistant Professor (1998-2001)
Postdoctoral Research Associate (1994-1998). Advisor: James J. Coleman

California Institute of Technology

Postdoctoral Research Fellow (1993-1994). Advisor: Nathan S. Lewis

University of California, Los Angeles

Research Assistant (1989-1993). Advisor: Robert L. Whetten

EDUCATION

Ph.D. in Chemistry, Physical Chemistry, University of California, Los Angeles, 1993
B.S. in Chemistry, Physical Chemistry, Peking University, China, 1986

HONORS and AWARDS

- Fellow, National Academy of Inventors (2021)
- Fellow, Big Ten Academic Alliance (BTAA) Academic Leadership Program (2020)
- Donald Bigger Willett Professor in Engineering, UIUC (2020)
- Fellow, Optical Society of America (2019)
- Fellow, American Physical Society (2018)
- Fellow, IEEE (2017)
- Deputy Editor, Applied Physics Letters (2015-)
- Board of governors, IEEE Photonics Society (2014-2016)
- Distinguished Lecturer, IEEE Nanotechnology Council (2014-2016)
- Faculty Entrepreneurial Fellow, Inaugural, College of Engineering, UIUC (2015-2016)

- Campus Excellent Teacher ranked by students, UIUC (2015, 2020)
- A. T. Yang Research Award, ECE, UIUC (2013)
- Dean's Award for Excellence in Research, College of Engineering, UIUC (2012)
- **ONR Young Investigator Program Award:** Massively Parallel Planar III-V Nanowires and Rolled-up Tubes: Novel Platforms for High Linearity Electronics and Integrated Photonic Circuits (2011-14)
- **DARPA Young Faculty Award:** III-V nanowire FinFET on silicon: a bottom-up CMOS compatible approach (2009-2011)
- **NSF CAREER Award:** Semiconductor nanotubes - new nanotechnology building blocks and functionalities (2008-2013)

PATENTS (3 Portfolios, 20 Awarded, 3+ Pending)

Patent Portfolio I: Metal-assisted chemical etching (MacEtch)

1. Metal-assisted chemical etching porous silicon formation method (Application number: 09/662,682, **issued** 09/14/2004). Inventors: Xiuling Li, Paul W. Bohn, Jonathan V. Sweedler
2. Metal-assisted chemical etching to produce porous group III-V materials (Application number: 09/989,050, **issued** 07/13/2004). Inventors: Paul W. Bohn, Xiuling Li, Jonathan V. Sweedler, Ilesanmi Adesida
3. Method of forming Nanoscale Three Dimensional Patterns in a Porous Material (Application number: 13/062,130, **issued** 07/16/2013). Inventors: Xiuling Li, David N. Ruzic, Ik Su Chun, Edmond K. C. Chow, Randolph E. Flauta
4. Metal-assisted chemical etching to produce III-V semiconductor nanostructures (Application number: 13/833,462, **issued** 02/10/2015). Inventors: Xiuling Li, Matthew T. Dejarld, Parsian Mohseni, Jae Cheol Shin, Winston Chern
5. Method of forming an array of high aspect ratio semiconductor nanostructures (Application number: 13/503,123, **issued** 03/17/2015). Inventors: Xiuling Li, Nicholas X. Fang, Placid M. Ferreira, Winston Chern, Ik Su Chun, Keng Hao Hsu
6. Apparatus and Method for Magnetic-Field Guided Metal-Assisted Chemical Etching, (Application number 14/541,765, **issued** 7/11/2017). Inventors: Xiuling Li, Weidong Zhou, Wen Huang
7. Self-anchored Catalyst Metal-assisted chemical Etching (Application number: 15/441,745, **issued** 07/30/2018). Inventors: Xiuling Li, Jeongdong Kim, Lingyu Kong, Munho Kim
8. Catalyst-Assisted Chemical Etching with a Vapor-Phase Etchant (Application number: 15/712,498, **issued** 02/28/2019). Inventors: Xiuling Li and Jeongdong Kim
9. Optoelectronic device including a buried metal grating for extraordinary optical transmission (EOT) (Application number: 15/200,345, **issued** 03/25/2019). Inventors: Xiuling Li, Daniel Wasserman, and Xiang Zhao
10. Optoelectronic Device Having an Antireflective Surface (Application number: 16/034,774, U.S. Patent No. 10,741,705, **issued** 08/11/2020). Inventors: Xiuling Li, Munho Kim, Jeong Dong Kim
11. Catalyst-Assisted Chemical Etching with a Vapor-Phase Etchant (Application number, 16/361,514 **issued** 05/2020). Inventors: Xiuling Li, Jeong Dong Kim, Dane J. Sievers, Lukas Janavicius

Patent Portfolio II: Strain-induced self-rolled-up membranes (S-RUM)

12. Rolled-up transformer structure for a radio frequency integrated circuit (RFIC) (Application number: 14/051,208, **issued** 01/27/2015). Inventors: Xiuling Li, Wen Huang
13. Rolled-up transmission line structure for a radio frequency integrated circuit (RFIC) (Application number: 14/051,192, **issued** 04/28/2015). Inventors: Xiuling Li, Wen Huang

14. Rolled-up inductor structure for a radio frequency integrated circuit (RFIC) (Application number: 14/051,188, **issued** 12/29/2015). Inventors: [Xiuling Li](#), Placid M. Ferreira, Wen Huang, Xin Yu
15. Tubular Resonant Filter and Method of Making a Tubular Resonant Filter (Application number: 15/092,979, **issued** 06/19/2018). Inventors: [Xiuling Li](#), Wen Huang, Moyang Li
16. Rolled-up power inductor and array of rolled-up power inductors for on-chip applications (Application number: 15/704,262, **issued** 03/2019). Inventors: [Xiuling Li](#) and Wen Huang
17. Helical antenna and method of modulating the performance of a wireless communications device (Application number: 15/408,893, **issued** 4/30/2019). Inventors: [Xiuling Li](#), Wen Huang, and Paul J. Froeter
18. Rolled-up magnetic component for on-chip applications and method of making a rolled-up magnetic component (Patent number: 11/031,456, **issued** 6/8/2021). Inventors: [Xiuling Li](#), Wen Huang, J. Ni, G. Eden, Z. Ou, and Q. Chen
19. On-Chip Nanoscale Storage System Using Chimeric DNA (Application number: 16/593,450, pending). Inventors: Olgica Milenkovic, Nagendra Athreya, Apratim Khandelwal, Jean-Pierre Leburton, Xiuling Li, Charles Schroeder, Kasra Tabatabaei, and Bo Li.
20. Electroplating method for enhancing the performance of rolled-up passive components (application number: 63/093,363, provisional patent filed 10/19/2020). Inventors: [Xiuling Li](#), Apratim Khandelwal, and Zhendong Yang.

Patent Portfolio III: Nanowires

21. Method of fabricating a planar semiconductor nanowire (Patent number: 12/989,558, **issued** 08/19/2014). Inventors: Xiuling Li, Seth A. Fortuna
22. Field effect transistor structure comprising a stack of vertically separated channel nanowires (Patent number: 13/896,537, **issued** 12/29/2015 and **licensed to a major global semiconductor company**). Inventors: Xiuling Li and Yi Song
23. Method of Forming 3D Multi-stack Nanowire and Nanosheet High Electron Mobility Transistors (provisional patent 63/171,443 filed 04/06/2021). Inventors: Xiuling Li and Shaloo Rakheja.

PEER-REVIEWED JOURNAL PUBLICATIONS (h-index 54 according to Google Scholar)

<https://scholar.google.com/citations?user=MQgv9XYAAAAJ&hl=en>

1. "Nonlocal Time-Resolved Terahertz Spectroscopy in the Near Field," A. Pizzuto, E. Castro-Camus, W. Wilson, W. Choi, X. Li, and D. M. Mittleman, *ACS Photonics*, accepted (2021). Doi: 10.1021/acsp Photonics.1c01367
2. "Homoepitaxial GaN Micropillar Array by Plasma-Free Photo-Enhanced MacEtch," C. Chan, S. Namiki, J. Hite, M. Mastro, S. B. Qadri, and X. Li, *J. Vac. Sci. Technol. A* 39, 053212 (2021). Doi: 10.1166/6.0001231 **Featured Article and Cover of Sept./Oct. Issue.**
3. "Monolithic Lateral pn Junction GaAs Nanowire Diodes via Selective Lateral Epitaxy," W. Choi, G. Zhang, H.-C. Huang, P. K. Mohseni, C. Zhang, J. D. Kim, and X. Li, *Nanotechnology*, accepted.
4. "Producing Silicon Carbide Micro and Nanostructures by Plasma-Free Metal-Assisted Chemical Etching," J. A Michaels, L. Janavicius, X. Wu, C. Chan, H.-C. Huang, S. Namiki, M. Kim, D. Sievers, and X. Li, *Ad. Func. Mater.* 2103298 (2021). <https://doi.org/10.1002/adfm.202103298>
5. "Elastocapillary Force Induced Alignment of Large Area Planar Nanowires," K. Jung, W. Choi, Wonsik, H.-C. Huang, J. D. Kim, K. Chabak, and X. Li, *ACS Appl. Mater. Interfaces*, (2021) doi: 10.1021/acsam.0c20289.
6. "Anti-reflective porous Ge by open-circuit and lithography-free metal-assisted chemical etching," *Appl. Surf. Sci.* (2021). <https://doi.org/10.1016/j.apsusc.2021.149083>.

7. "Enhancing Performance of GaAs Photodiodes via Monolithic Integration of Self-Formed Graphene Quantum Dots and Antireflection Surface Texturing," S. Namiki, H.-C. Huang, J. Soares, X. Wu, J. D. Kim, B. Jiang, V. Srikumar, and X. Li, *Adv. Photon. Res.* doi: 10.1002/adpr.202000134.
8. "Au-Free Low-Temperature Ohmic Contacts for AlGaIn/AlN/GaN Heterostructures," *J. Vac. Sci. Technol. B* 38, 062206 (2020); <https://doi.org/10.1116/6.0000287>.
9. "Monolithic Heterogeneous Integration of 3D Microwave L-C Elements by Self-Rolled-up Membrane Nanotechnology," Z. Yang, M. D. Kraman, Z. Zheng, H. Zhao, J. Zhang, S. Gong, Y. V. Shao, W. Huang, P. Wang, and X. Li, *Adv. Func. Mater.* 30, 2004034 (2020). **First experimental demonstration of monolithic rolled-up L-C network.**
10. "Kirigami Inspired Self-assembly of 3D Structures," A. M. Abdullah, X. Li, P. V. Braun, J. A. Rogers, and K. J. Hsia, *Adv. Funct. Mater.* 1909888 (2020). doi.org/10.1002/adfm.201909888
11. "Hybrid Integration of n-MoS₂/p-GaN Diodes by Quasi-van der Waals Epitaxy," C.-Y. Liu, H.-C. Huang, W. Choi, J. Kim, K. Jung, W. Sun, N. Tansu, W. Zhou, H.-C. Kuo, and X. Li, *ACS Appl. Electron. Mater.* 2, 419-425 (2020). DOI: 10.1021/acsaelm.9b00607
12. "Monolithic Tesla Level Magnetic Induction by Self-Rolled-up Membrane Technology," W. Huang, Z. Yang, M. D. Kraman, Q. Wang, Z. Ou, M. M. Rojo, A. S. Yalamathy, V. Chen, F. Lian, J. H. Ni, S. Liu, H. Yu, L. Sang, J. Michaels, D. J. Sievers, J. G. Eden, P. V. Braun, Q. Chen, S. Gong, D. G. Senesky, E. Pop, and X. Li, *Sci. Adv.* 6, eaay4508 (2020).
13. "Effect of Perforation on the Thermal and Electrical Breakdown of Self-Rolled-up Nanomembrane Structures," J. A. Michaels, D. Wood, P. Froeter, W. Huang, D. Sievers, and X. Li, *Adv. Mater. Interfaces*, 6, 1901022 (2019). DOI: 10.1002/admi.201901022
14. "Ultrathin Silicon Nanomembrane in a Tubular Geometry for Enhanced Photodetection," C. Xu, R. Pan, Q. Guo, X. Wu, G. Li, G. Huang, Z. An, X. Li, and Y. F. Mei, *Adv. Opt. Mater.* 7, 1900823 (2019).
15. "CMOS-Compatible Catalyst for MacEtch: Titanium Nitride Assisted Chemical Etching in Vapor Phase for High Aspect Ratio Silicon Nanostructures," J.D. Kim, M. Kim, C. Chan, N. Draeger, J.J. Coleman, and X. Li, *ACS Appl. Mater. Interfaces*, 11, 27371-27377 (2019). doi: 10.1021/acsaami.9b00871.
16. "High Aspect Ratio β -Ga₂O₃ Fin Arrays with Low Interface Charge Density by Inverse Metal-Assisted Chemical Etching," H.-C. Huang, M. Kim, X. Zhan, K. Chabak, J. D. Kim, A. Kvit, D. Liu, Z. Ma, J.-M. Zuo, and X. Li, *ACS Nano*, 13, 8, 8784-8792 (2019). <https://doi.org/10.1021/acsnano.9b01709>.
17. "Monolithic radio frequency SiN_x self-rolled-up nanomembrane interdigital capacitor modeling and fabrication" by L. Sang, H. Zhou, Z. Yang, M. Kraman, H. Zhao, J. Michaels, D. Sievers, J. Schutt-Aine, X. Li, W. Huang, *Nanotechnology*, 30, 364001 (2019).
18. "Laterally confined photonic crystal surface emitting laser incorporating monolayer tungsten disulfide," X. Ge, M. Minkov, S. Fan, X. Li, and W. Zhou, *npj 2D Mater. App.* 3, 16 (2019).
19. "Reconfigurable nanoscale soft materials," Z. Ou, A. Kima, W. Huang, P. V. Braun, X. Li, and Q. Chen, *Curr. Opin. Solid State Mater. Sci.* 2018. DOI: 10.1016/j.cossms.2018.12.002
20. "Nanoscale Groove Textured β -Ga₂O₃ by Room Temperature Inverse Metal-assisted chemical Etching and Photodiodes with Enhanced Responsivity," M. Kim, H.C. Huang, J.D. Kim, K.D. Chabak, A.R.K. Kalapala, W. Zhou, and X. Li, *Appl. Phys. Lett.* 113 (22), 222104 (2018). <https://doi.org/10.1063/1.5053219>
21. "Colloidal Metal–Organic Framework Hexapods Prepared from Postsynthesis Etching with Enhanced Catalytic Activity and Rollable Packing," Z. Ou, X. Song, W. Huang, X. Jiang, S. Qu, Q. Wang, P. V. Braun, J. S. Moore, X. Li, and Q. Chen, *ACS Appl. Mater. Interfaces*, 10 (48), 40990 (2018). DOI: 10.1021/acsaami.8b17477

22. "Aligning Synthetic Hippocampal Neural Circuits via Self-Rolled-Up Silicon Nitride Microtube Arrays," O. Cangellaris, E. Corbin, P. Froeter, J. Michaels, X. Li, and M. Gillette, *ACS Appl. Mater. Interfaces*, 10 (42), 35705 (2018). DOI: 10.1021/acsami.8b10233
23. "Enhanced performance of Ge photodiodes via monolithic antireflection texturing and α -Ge self-passivation by inverse metal-assisted chemical etching," M. Kim, S. Yi, J. D. Kim, X. Yin, J. Li, J. Bong, D. Liu, S.-C. Liu, A. Kvit, W. Zhou, X. Wang, Z. Yu, Z. Ma, and X. Li, *ACS Nano*, 12 (7), 6748 (2018). DOI: 10.1021/acsnano.8b01848
24. "Anisotropic Rolling and Controlled Chirality of Nanocrystalline Diamond Nanomembranes toward Biomimetic Helical Frameworks," Z. Tian, W. Huang, B. Xu, X. Li, and Y. Mei, *Nano Lett.* 18 (6), pp 3688–3694, (2018). DOI: 10.1021/acs.nanolett.8b00828
25. "Self-folded Gripper-like Architectures from Stimuli-responsive Bilayers," A. Abdullah, P. Braun, J.A. Rogers, X. Li, and K.J. Hsia, *Adv. Mater.* (2018). DOI: 10.1002/adma.201801669.
26. "Three-dimensional radio frequency transformers based on a self-rolled-up membrane platform," W. Huang, J. Zhou, P. Froeter, K. Walsh, S. Liu, M. Li, J. Michaels, D. Sievers, S. Gong, and X. Li, *Nature Electronics*, 1, 305-313 (2018). DOI: 10.1038/s41928-018-0073-5 **First experimental demonstration of rolled-up transformers.**
27. "Low index contrast heterostructure photonic crystal cavities with high quality factors and vertical radiation coupling," X. Ge, M. Minkov, S. Fan, X. Li, and W. Zhou, *Appl. Phys. Lett.* 112, 141105 (2018).
28. "Self-Anchored Catalyst Interface Enables Ordered Via Array Formation from Sub-micron to millimeter Scale for Poly- and Single-Crystalline Silicon," J.D. Kim, M. Kim, L. Kong, P. Mohseni, S. Ranganathan, J. Pachamuthu, W.K. Chim, S.Y. Chiam, J.J. Coleman, and X. Li, *ACS Appl. Mater. Interfaces*, 10 (10), pp 9116–9122 (2018).
29. "Downscaling inductors with graphene," *Nature Electronics, News and Views*, W. Huang and X. Li, 1(1), 6 (2018).
30. "Passive wavelength tuning and multichannel photonic coupling using monolithically integrated vertical micro resonators on ridge waveguides," X. Yu, L. L. Goddard, J. Zhu, X. Li, and X. Chen, *Appl. Phys. Lett.*, 112, 021108 (2018).
31. "Morphable 3D Mesostructures and Microelectronic Devices by Multistable Buckling Mechanics," H. Fu, K. Nan, W. Bai, W. Huang, K. Bai, L. Lu, C. Zhou, Y. Liu, F. Liu, J. Wang, M. Han, Z. Yan, H. Luan, Y. Zhang, Y. Zhang, J. Zhao, X. Cheng, M. Li, J. W. Lee, Y. Liu, D. Fang, X. Li, Y. Huang, Y. Zhang, and J. A. Rogers, *Nature Mater.* 17, 268–276 (2018). doi:10.1038/s41563-017-0011-3.
32. "AlGaAs/Si Dual-junction Tandem Solar Cells by Epitaxial Lift-off and Print Transfer-Assisted Direct Bonding," K. Xiong, H. Mi, T. Chang, D. Liu, Z. Xia, M. Wu, X. Yin, S. Gong, W. Zhou, J. C. Shin, X. Li, M. Arnold, X. Wang, H.-C. Yuan, and Z. Ma, *Energy Sci Eng.* 6(1), 47-55 (2018). doi:10.1002/ese3.182.
33. "Enhanced Color-Conversion Efficiency of Hybrid Nanostructured-Cavities InGaN/GaN Light-Emitting Diodes Consisting of Nontoxic InP Quantum Dots," C.-Y. Liu, T.-P. Chen, J.-K. Huang, T.-N. Lin, C.-Y. Huang, X. Li, H. -C. Kuo, J.-L. Shen, C.-Y. Chang, *IEEE J. Selected Topics Quantum Electron.* 23 (5), 1-7 (2017).
34. "Damage-Free Smooth-Sidewall InGaAs Nanopillar Array by Metal-Assisted Chemical Etching," L. Kong, Y. Song, J. D. Kim, L. Yu, D. Wasserman, W. K. Chim, S. Y. Chiam, and X. Li, *ACS Nano*, 11 (10), pp 10193-10205 (2017).
35. "A Review of III-V Planar Nanowire Arrays: Selective Lateral VLS Epitaxy and 3D Transistors," C. Zhang, X. Miao, K. Chabak, and X. Li, *J. Phys. D: Appl. Phys.* 50, 393001 (2017).

36. "Minimizing Isolate Catalyst Motion in Metal-Assisted Chemical Etching for Deep Trenching of Silicon Nanohole Array," L. Kong, Y. Zhao, B. Dasgupta, Y. Ren, K. Hippalgaonkar, X. Li, W. K. Chim, and S. Y. Chiam, *ACS Appl. Mater. Interfaces*, 9 (24), pp 20981–20990 (2017).
37. "Printing, Folding and Assembly Methods for 3D Mesostructures in Advanced Materials" Y. Zhang, F. Zhang, Z. Yan, Q. Ma, X. Li, Y. Huang, and J. A. Rogers, *Nature Rev. Mater.* 2, 1709 (2017). **Selected as cover.**
38. "Mechanically-guided deterministic assembly of 3D mesostructures assisted by residual stresses," H. Fu, K. Nan, P. Froeter, W. Huang, Y. Liu, Y. Wang, J. Wang, Z. Yan, H. Luan, X. Guo, Y. Zhang, C. Jiang, L. Li, A. C. Dunn, X. Li, Y. Huang, Y. Zhang, J. A. Rogers, *Small*, 13, 1700151 (2017).
39. "Direct Electrical Probing of Periodic Modulation of Zn-Dopant Distributions in Planar VLS GaAs Nanowires," W. Choi, E. Seabron, P.K. Mohseni, J.D. Kim, T. Gokus, A. Cernescu, P. Pochet, H. Johnson, W.L. Wilson, and X. Li, *ACS Nano*, 11 (2), pp 1530–1539 (2017).
40. "Scaling the Aspect Ratio of Nanoscale Closely-Packed Silicon Vias by MacEtch: Kinetics of Carrier Generation and Mass Transport," J.D. Kim, P.K. Mohseni, K. Balasundaram, S. Ranganathan, J. Pachamuthu, J.J. Coleman, X. Li, *Adv. Funct. Mater.* 27, 1605614 (2017).
41. "Enhancement-Mode Ga₂O₃ Wrap-Gate Fin Field-Effect Transistors on Native (100) β-Ga₂O₃ Substrate with High Breakdown Voltage," K. D. Chabak, N. Moser, A. J. Green, D. E. Walker Jr., S. E. Tetlak, E. Heller, A. Crespo, R. Fitch, J. McCandless, K. Leedy, M. Baldini, G. Wagner, Z. Galazka, X. Li, G. Jessen, *Appl. Phys. Lett.* 109, 213501 (2016). **Selected as cover.**
42. "Evidences for redox reaction driven charge transfer and mass transport in metal-assisted chemical etching of silicon," L. Kong, B. Dasgupta, Y. Ren, P. K. Mohseni, M. Hong, X. Li, W. K. Chim, and S. Y. Chiam, *Sci. Rep.* 6, 36583 (2016).
43. "Direct Observation of Dopants Distribution and Diffusion in GaAs Planar Nanowires with Atom Probe Tomography," J. Qu, W. Choi, P. K. Mohseni, X. Li, Y. Zhang, H. Chen, S. Ringer, and R. Zheng, *Appl. Mater. Interfaces*, 8 (39), 26244–26250 (2016).
44. "Enhanced axial confinement in a monolithically integrated self-rolled-up SiN_x vertical microring photonic coupler," X. Yu, L. L. Goddard, X. Li and X. Chen, *Appl. Phys. Lett.* 109, 111104 (2016).
45. "Ultra-High Aspect Ratio InP Junctionless FinFETs by a Novel Wet Etching Method," Y. Song, P. K. Mohseni, S. H. Kim, J. C. Shin, T. Ishihara, I. Adesida, and X. Li, *IEEE Electron Dev. Lett.* 37(8), 970-973 (2016). **First report on record high aspect ratio InP FinFET by inverse-MacEtch.**
46. "III-V Nanowire Transistors for Low-Power Logic Applications: a Review and Outlook," C. Zhang and X. Li, *IEEE Trans. Electron Dev.* 63(1), 223 (2016).
47. "Enhanced Optical Transmission Through MacEtch-Fabricated Buried Metal Gratings," R. Liu, X. Zhao, C. Roberts, L. Yu, P. Mohseni, X. Li, V. Podolskiy, and D. Wasserman, *Adv. Mater.* 28, 1441-1448 (2016).
48. "A Large-Area, Uniform White Light LED Source on A Stretchable Substrate," CW Sher, KJ Chen, CC Lin, HV Han, HY Lin, ZY Tu, HH Tu, K Honjo, HY Jiang, SL Ou, RH Horng, X. Li, CC Fu, and HC Kuo, *Optics Express*, 23 (19), A1167-A1178 (2015).
49. "Monolithically integrated self-rolled-up microtube-based vertical coupler for 3D photonic integration," X. Yu, E. Arbabi, L. L. Goddard, X. Li and X. Chen, *Appl. Phys. Lett.* 107, 031102 (2015).
50. "InAs Planar Nanowire Gate-All-Around MOSFETs on GaAs Substrates by Selective Lateral Epitaxy," C. Zhang, W. Choi, P. Mohseni, and X. Li, *IEEE Electron Dev. Lett.* 36, 633 (2015).
51. "Quenched Phonon Drag in Silicon Nanowires Reveals Significant Effect in the Bulk at Room Temperature," J. Sadhu, H. Tian, J. Ma, B. Azeredo, J. Kim, K. Balasundaram, C. Zhang, X. Li, P. Ferreira, and S. Sinha, *Nano Lett.* 15 (5), 3159–3165 (2015).

52. "RF Performance of Planar III-V Nanowire-Array Transistors Grown by Vapor-Liquid-Solid Epitaxy," K. D. Chabak, X. Miao, C. Zhang, D. E. Walker Jr., P. K. Mohseni, and X. Li, *IEEE Electron Dev. Lett.* 36(5), 445-447 (2015).
53. "Evolution of GaAs Nanowire Geometry in Selective Area Epitaxy," K. P. Bassett, P.K. Mohseni, and X. Li, *Appl. Phys. Lett.* 106, 133102 (2015).
54. "Ultra-Small, High-Frequency, and Substrate-Immune Microtube Inductors Transformed from 2D to 3D," X. Yu, W. Huang, M. Li, T. M. Comberiate, S. Gong, J. E. Schutt-Aine, and X. Li, *Sci. Rep.* 5, 9661 (2015). **First experimental demonstration of rolled-up inductors.**
55. "Wide-Range Correlated Color Temperature Light Generation from Resonant Cavity Hybrid Quantum Dot Light Emitting Diodes," K.-J. Chen, C.-C. Lin, H.-V. Han, C.-Y. Lee, S.-H. Chien, K.-Y. Wang, S.-H. Chiu, Z.-Y. Tu, J.-R. Li, T.-M. Chen, X. Li, M.-H. Shih, and H.-C. Kuo, *J. Selected Topics in Quantum Electronics*, 21(4), 1900407 (2015).
56. "An Analytical Metal Resistance Model and Its Application for Sub-22nm Metal Gate CMOS," X. Miao, R. Bao, U. Kwon, K. Wong, W. Rausch, J. Bruley, P. DeHaven, W. Weng, R. Wachnik, R. Divakaruni, S. Grunow, M. Chudzik, V. Narayanan, X. Li, and S. Krishnan, *IEEE Electron Device Lett.* 36(4), 384-386 (2015).
57. "Efficient Hybrid White Light-emitting Diodes by Organic-Inorganic materials at different CCT from 3000K to 9000K," K.-J. Chen, Y.-C. Lai, B.-C. Lin, C.-C. Lin, S.-H. Chiu, Z.-Y. Tu, M.-H. Shih, P. Yu, P.-T. Lee, X. Li, H.-F. Meng, G.-C. Chi, T.-M. Chen, and H.-C. Kuo, *Optics Express*, 23 (7), A204–A210 (2015).
58. "Assembly of Micro/Nanomaterials into Complex, Three-dimensional Architectures by Compressive Buckling," S. Xu, Z. Yan, K. Jang, W. Huang, H. Fu, J. Kim, Z. Wei, M. Flavin, J. McCracken, R. Wang, A. Badea, H. Liu, D. Xiao, G. Zhou, J. Lee, H. U. Chung, H. Cheng, W. Ren, A. Banks, X. Li, U. Paik, R. G. Nuzzo, Y. Huang, Y. Zhang and J. A. Rogers, *Science*, 347 (6218), 154-159 (2015).
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BOOK CHAPTERS

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4. "Spatially resolved optical characterization of GaN structures produced by selective area epitaxial lateral overgrowth," X. Li, P.W. Bohn, Y. W. Kim, and J. J. Coleman, in "III-Nitride semiconductors: Growth" Optoelectronic Properties of Semiconductors and Superlattices; v. 19, edited by M.O. Manasreh and I.T. Ferguson. New York: Taylor & Francis, 2003.

BULLETINS

1. "MacEtch: anisotropic metal assisted chemical etching defies the textbook," Xiuling Li, SPIE Newsroom, DOI: 10.1117/2.1201203.004147, March 28, 2012.
2. "Coaxial nanowires from van der Waals epitaxy," Xiuling Li and Parsian Mohseni, SPIE newsroom, DOI: 10.1117/2.1201309.005122, September 23, 2013.

CONFERENCE PROCEEDINGS (PARTIAL LIST):

1. C.-H. Huang, Z. Ren, A. Green, K. Chabak, and X. Li, "Metal-Assisted Chemical Etching of (001) Beta-Ga₂O₃," Compound Semiconductor Week, Sweden (virtual), May 2021.
2. S. Namiki, Y. H. Song, J. C. Shin, and X. Li, "Metal-Assisted Chemical Etching of InGaAsP For Distributed Feedback Laser Gratings," Compound Semiconductor Week, Sweden (virtual), May 2021.
3. C. Y. Chan, S. Namiki, J. K. Hite, and X. Li, "Photoinduced Open-circuit Metal-assisted Chemical Etching of Homoepitaxial GaN," Compound Semiconductor Week, Sweden (virtual), May 2021.
4. C. Y. Chan, S. Namiki, J. K. Hite, and X. Li, "Plasma-free Anisotropic Etching of GaN," CLEO, virtual, May 2021.
5. W. Choi, S. Fan, P. Mohseni, M. L. Lee, and X. Li, "Selective area epitaxy of GaP nanowire array on Si (111) by MOCVD," Compound Semiconductor Week, Nara, Japan, May 2019.
6. H.-C. Huang, M. Kim, X. Zhan, K. Chabak, J. D. Kim, J.-M. Zuo and X. Li, "On the Surface Properties of High Aspect Ratio β -Ga₂O₃ Fin Structures Formed by I-MacEtch," Compound Semiconductor Week, Nara, Japan, May 2019.

7. C.-Y. Liu, W. Choi, H. Huang, J.D. Kim, W. Zhou, W. Sun, N. Tansu, H. Kuo, and X. Li, "Pseudo-van der Waals Epitaxy of MoS₂ on Patterned and Planar GaN Substrates," CLEO, San Jose, CA, May 2019.
8. M. Kim, S. Yi, J. D. Kim, X. Yin, J. Li, J. Bong, D. Liu, S.-C. Liu, A. Kvit, W. Zhou, X. Wang, Z. Yu, Z. Ma, and X. Li, "Germanium photodiodes on pyramidal textured surface by Metal-Assisted Chemical Etching," CLEO, San Jose, CA, May 2019.
9. H-C Huang, M. Kim, K. Chabak, and X. Li, "Metal-assisted chemical Etching of β -Ga₂O₃ and Textured MSM Photodetectors with Enhanced Responsivity," IEEE RAPID, Florida (2018).
10. H.-C. Huang, M. Kim, K. Chabak, and X. Li, "Metal-assisted chemical etch of β -Ga₂O₃ and applications," 3rd US Workshop on Gallium Oxide (GOX 2018), Columbus, Ohio, July 2018.
11. M. Kim, H.-C. Huang, K. Chabak, and X. Li, "Metal-assisted chemical etch of β -Ga₂O₃: towards the formation of smooth, vertical, damage-free, and high aspect ratio fin array," Compound Semiconductor Week 2018, Boston, USA (2018).
12. Olivia V. Cangellaris, Elise A. Corbin, Paul Froeter, Xiuling Li, Martha U. Gillette, "Tuning Topography of Self-Rolled-Up 3D Microtube Arrays to Improve Alignment of Hippocampal Neurons," Biomedical Engineering Society (BMES) Annual Meeting, Phoenix, October 2017.
13. M Kim, S Yi, JD Kim, SC Liu, W Zhou, Z Yu, and X Li, "Nano-indented Ge surfaces by metal-assisted chemical etching (MacEtch) and its application for optoelectronic devices," Device Research Conference (DRC), 2017 75th Annual, 1-2, Norte Dame, 2017.
14. W. Huang, J. Zhou, P. Froeter, K. Walsh, S. Liu, J. Michaels, M. Li, S. Gong, and X. Li, "CMOS-Compatible On-Chip Self-Rolled-Up Inductors for RF/mm-Wave Applications," International Microwave Symposium, 1645-1648, Hawaii, 6/4/2017.
15. K. Jung, CY Liu, J.D. Kim, W. Choi, W. Zhou, H.-C. Kuo, and X. Li, "Large Area MoS₂ van der Waals Epitaxy on III-Ns and the Epitaxial Formation of a n-MoS₂/p-InGaN Diode," IEEE Photonics Conference, Hawaii, 2016.
16. X. Li, K. Chabak, W. Choi, J. Kim, X. Miao, P. Mohseni, Y. Song, D.E.Walker, C. Zhang, "III-V Nanowires and Nano fins: Growth, Etching, and Devices," International Conference on Solid State Devices and Materials (SSDM), Tsukuba, 2016.
17. C. Zhang, W. Choi, P. Mohseni and X. Li, "InAs Nanowire Gate-All-Around MOSFETs by Heterogeneous Planar VLS Growth," Device Research Conference, the Ohio State University, 2015.
18. Y. Song, P. K. Mohseni, S. H. Kim, J. C. Shin, C. Zhang, K. Chabak, and X. Li, "InP FinFETs with Damage-Free and Record High-Aspect-Ratio (45:1) Fins Fabricated by Metal-Assisted Chemical Etching," Device Research Conference, the Ohio State University, 2015.
19. P. Froeter, Y. Huang, O. V. Cangellaris, W. Huang, M. U. Gillette, J. Williams, and X. Li, "Superior Neuronal Outgrowth Guidance and Rate Enhancement using Silicon Nitride Self-Rolled-up Membranes," Device Research Conference, the Ohio State University, 2015.
20. W. Huang, M. Li, S. Gong, and X. Li, "Self-rolled-up Tube Transformers: Extreme Miniaturization and Performance Enhancement," Device Research Conference, p. 223-224, Columbus Ohio, June 2015.
21. M.U. Gillette, A. Jain, O.V. Cangellaris, S.C. Liu, R. Iyer, L.J. Millet, T. Kim, P. Froeter, M.K. Lee, A. Abdeen, K. Kilian, G. Popescu, H. Kong, and X. Li, "Enabling Technologies for Neurons: New Approaches for Axonal and Dendritic Growth and Guidance," Biomedical Engineering Society Meeting, 2015.
22. W. Huang, M. Li, S. Gong, and X. Li, "Extremely Miniaturized and High Performance RFIC Transformer by Self-rolled-up Membrane Nanotechnology," InterPACKICNMM, San Francisco, July 2015.

23. X. Yu, E. Arbabi, X. Li, L. Goddard, and X. Chen, "Demonstration of the first monolithically integrated self-rolled-up tube based vertical photonic coupler," CLEO 2015, DOI: 10.1364/CLEO_SI.2015.SF1H.8
24. Jeongdong Kim, Xiaogang Chen, Xiuling Li, and James Coleman, "Photocurrent Density Enhancement of a III-V Inverse Quantum Dot Intermediate Band Gap Photovoltaic Device," CLEO 2015. DOI: 10.1364/CLEO_AT.2015.JTu5A.65
25. K. Chabak, X. Miao, C. Zhang, D. E. Walker Jr., and X. Li, "RF Performance of 3D III-V Nanowire T-Gate HEMTs Grown by VLS Method," Device Research Conference (DRC), 72nd Annual, pages 211-212, June 2014.
26. W. Huang, X. Yu, T. Comberiate, C.-W. Qiu, J. E. Schutt-Aine, and X. Li, "Miniaturized on-chip passive devices based on self-rolled-up SiN_x nanomembrane inductive tube," Device Research Conference (DRC), 71st Annual, pages 227-228, June 2013.
27. C. Zhang, R. Dowdy, and X. Li, "High voltage gain MESFET amplifier using self-aligned MOCVD grown planar GaAs nanowires," Device Research Conference (DRC), 71nd Annual, pages 63-64, June 2013.
28. A.D.K. Finck, D.J. Van Harlingen, P.K. Mohseni, K. Jung, and X. Li, "Hints of hybridizing Majorana fermions in a nanowire coupled to superconducting leads," APS march Meeting, v. 58 (1), 2013.
29. H. Hu, P. K. Mohseni, X. Li, M. A. Shannon, and W. P. King, "Nanoelectromechanical systems fabricated using tip-based nanofabrication," Proceedings of SPIE, Baltimore, Volume 8725, Pages 8725-23, 2013.
30. X. Li, "Twinning Superlattice in VLS Grown Planar GaAs Nanowires Induced by Impurity Doping," IEEE Photonics Conference, San Francisco, Sept. 2012.
31. P. K. Mohseni, J. C. Shin, and X. Li, "Wafer-Scale InGaAs Nanowire Epitaxy on Si and Graphene Substrates," International Symposium on Compound Semiconductors, Santa Barbara, CA, Aug. 2012.
32. P. K. Mohseni, K. Balasundaram, M. DeJarld, J. C. Shin, D. Chanda, J. A. Rogers, and X. Li, "Anisotropic Metal-Assisted-Chemical-Etching for III-V Compound Semiconductors," International Symposium on Compound Semiconductors, Santa Barbara, CA, Aug. 2012.
33. X. Miao, C. Zhang, and X. Li, "Short-channel enhancement-mode planar GaAs nanowire HEMTs through a bottom-up method," Device Research Conference (DRC), 2012 70th Annual, 18-20 June 2012.
34. J. C. Shin, P. K. Mohseni, S. Tomasulo, K. Montgomery, M. Lee, and X. Li, "Heterogeneous Integration of III-V on Si: overcoming the lattice-mismatch barrier via the 1D route," CLEO, San Jose, May 2012.
35. R. Dowdy, K. Attenkofer, H. Frisch, S.W. Lee, S. Ross, and X. Li, "Development of Ultra-Thin GaAs Photocathodes," TIPP 2011, Physics Procedia, 2012.
36. X. Miao and X. Li, "Planar VLS Grown GaAs Nanowire Array Based HEMTs," International Semiconductor Device Research Symposium (ISDRS), Maryland, Dec. 2011
37. W. Chern, K. J. Yu, D. Chanda, J. C. Shin, J. A. Rogers, and X. Li, "Ordered silicon nanowire array based solar cells produced by metal assisted chemical etching," 2010 23rd Annual Meeting of the IEEE Photonics Society, Denver, CO, 2010, pp. 718-719.
38. W. Chern, K. Hsu, I. Chun, B. Azeredo, N. Fang, P. Ferreira, and X. Li, "Non-lithographic Patterning and Metal-Assisted Chemical Etching for Manufacturing of Tunable Light-Emitting Silicon Nanowire Arrays," 2010 Conference on Lasers and Electro-Optics (CLEO), San Jose, CA, 2010.
39. I. S. Chun, K. P. Bassett, A. Challa, X. Miao, M. Saarinen, and X. Li, "Strain-induced Self-rolling III-V Tubular nanostructures: Formation Process and Photonic Applications," Proc. of SPIE, 7608, 760810, (2010); invited paper.

40. N. L. Dias, A. Garg, J. D. Young, U. Reddy, V. B. Verma, K. Bassett, X. Li, and J. J. Coleman, "Reduced scattering rate in nanopore structures," 2010 23rd Annual Meeting of the IEEE Photonics Society, Denver, CO, 2010, pp. 84-85.
41. S. A. Fortuna, R. Dowdy, and X. Li, "Epitaxial III-V planar nanowires: Self-aligned, high-mobility and transfer-printable," Conference Proceedings, International Conference on Indium Phosphide and Related Materials, Japan, 2010, pp. 88-91.
42. V. B. Verma, N. L. Dias, U. Reddy, K. P. Bassett, X. Li, and J. J. Coleman, "Bandstructure engineering with a two-dimensional patterned quantum dot lattice," Lasers and Electro-Optics/Quantum Electronics and Laser Science Conference: 2010 Laser Science to Photonic Applications, CLEO/QELS 2010, San Jose, CA, 2010.
43. V. B. Verma, U. Reddy, N. L. Dias, K. P. Bassett, X. Li, and J. J. Coleman, "Patterned quantum dot molecule laser fabricated by electron beam lithography and wet chemical etching," 2010 IEEE Photonics Society Winter Topicals Meeting Series, WTM 2010, Majorca, Spain, 2010, pp. 143-144.
44. S. A. Fortuna, I. S. Chun, J. Wen, R. Dowdy, and X. Li, "GaAs 110 nanowires: Planar, self-aligned, twin-free, high-mobility and transfer-printable," The 14th OptoElectronics and Communications Conference (OECC 2009), Hong Kong, China, 2009.
45. S. A. Fortuna and X. Li, "GaAs FET with a high mobility self-assembled planar nanowire channel on a (100) substrate," Device Research Conference, University Park, PA, 2009, pp. 19-20.
46. I. S. Chun, K. Bassett, A. Challa, and X. Li, "Ultra-thin-walled III-arsenide microtubes with embedded QW light emitters: room temperature PL characteristics," CLEO, late news paper, Baltimore, Maryland, 2009, p. 2.
47. I. S. Chun, E. K. Chow, and X. Li, "3D Nanoscale pattern formation in porous silicon," CLEO, San Jose, CA, United states, 2008, p. 2.
48. S. A. Fortuna, J. Wen, and X. Li, "MOCVD grown III-V nanowires: In-plane, self-aligned and transfer-printable," IEEE LEOS Annual Meeting, Newport Beach, CA, United states, 2008, pp. 429-430.
49. S. A. Fortuna, X. Zeng, and X. Li, "Self-aligned planar GaAs nanowires grown by MOCVD on GaAs (100) substrates," CLEO, San Jose, CA, United states, 2008.
50. I.S. Chun, V.B. Verma, and X. Li, "Engineered large area fabrication of ordered InGaAs-GaAs nanotube arrays," *Mater. Res. Soc. Symp. Proc.* **1057**, II05-40, 2008.
51. S. Kim, X. Li, J. J. Coleman, R. Zhang, D. M. Hansen, T. F. Kuech, and S. G. Bishop, "Photoluminescence and photoluminescence excitation spectroscopy of in situ Er-doped and Er-implanted GaN films grown by hydride vapor phase epitaxy," *MRS Internet Journal of Nitride Semiconductor Research*, vol. 4S1, 1999.
52. S. Kim, X. Li, J. J. Coleman, R. Zhang, D. M. Hansen, T. F. Kuech, and S. G. Bishop, "Photoluminescence and photoluminescence excitation spectroscopy of in situ Er-doped and Er-implanted GaN films grown by hydride vapor phase epitaxy," Warrendale, PA, USA, 1999, pp. 11-4.
53. X. Li, S. G. Bishop, and J. J. Coleman, "GaN: from selective area to epitaxial lateral overgrowth," Boston, MA, USA, 1999, pp. G4.8-G4.8.
54. X. Li, S. G. Bishop, and J. J. Coleman, "GaN: from selective area to epitaxial lateral overgrowth," *MRS Internet Journal of Nitride Semiconductor Research*, vol. 4S1, 1999.
55. J. J. Coleman and T. S. Yeoh, "Metalorganic vapor phase epitaxy of quantum dots," Piscataway, NJ, USA, 2001, pp. 560-1.
56. S. Kim, S. J. Rhee, X. Li, J. J. Coleman, and S. G. Bishop, "Site-selective photoluminescence spectroscopy of Er-implanted wurtzite GaN under various annealing conditions," Warrendale, PA, USA, 1998, pp. 381-6.

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60. S. Kim, S. J. Rhee, D. A. Turnbull, X. Li, J. J. Coleman, and S. G. Bishop, "Site-selective photoluminescence excitation and photoluminescence spectroscopy of Er-implanted wurtzite GaN," San Francisco, CA, USA, 1997, pp. 131-136.
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62. X. Li, S. Kim, S. G. Bishop, and J. J. Coleman, "On the incorporation of arsenic (As) in GaN films by conventional MOCVD," Montreal, Can, 1997, p. 8.
63. S. J. Rhee, S. Kim, X. Li, J. J. Coleman, and S. G. Bishop, "Photoluminescence quenching spectroscopy of trap-mediated Er³⁺ excitation mechanisms in Er-implanted GaN," Boston, MA, USA, 1997, pp. 667-672.
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65. X. Li, A.M. Jones, S.D. Roh, D.A. Turnbull, E.E. Reuter, S.Q. Gu, S.G. Bishop and J.J. Coleman, "Correlation of surface morphology and optical properties of GaN by conventional and selective-area MOCVD," *Mat. Res. Soc. Symp. Proc.* 395, 943 (1996).
66. X. Li, A. M. Jones, S. D. Roh, D. A. Turnbull, E. E. Reuter, S. Q. Gu, S. G. Bishop, and J. J. Coleman, "Correlation of surface morphology and optical properties of GaN by conventional and selective-area MOCVD," Boston, MA, USA, 1996, pp. 943-948.

PRESENTATIONS

Invited Conference talks, seminars, and short courses (> 128)

1. "Plasma-free Anisotropic etching of SiC, GaN, and α -Ga₂O₃," 2021 NASA Glenn Silicon Carbide (SiC) Materials & Devices Workshop, virtual, Sept. 2021.
2. "Transform electronics, photonics, and biomedical research by nanofabrication," virtual guest lecture, Nazarbayev University, Kazakhstan, April 2021.
3. "Extreme Miniaturization of Passive Electronic Components by Strain-Induced Self-Rolled-Up Membrane Nanotechnology," keynote speaker, MRS, virtual presentation, Nov. 2020.
4. "MacEtch of β -Ga₂O₃, SiC and GaN—Plasma-Damage-Free and High-Aspect-Ratio," invited speaker, MRS, virtual presentation, Nov. 2020.
5. "Self-rolled-up membrane nanotechnology for passive electronic component miniaturization and integration," keynote speaker, 15th IEEE Nanotechnology Materials and Devices Conference (NMDC), virtual presentation, Oct. 2020.
6. "Transform electronics, photonics, and biomedical research by nanofabrication," Global Online Science Talk, iCANX, virtual presentation, May 2020.

7. “ β -Ga₂O₃: MacEtch, Passivation, and Ultra-wide Bandgap Photonic Devices,” invited speaker, CLEO, virtual presentation, May 2020.
8. “MacEtch of Wide Bandgap Semiconductors: Plasma Free and High Aspect Ratio,” invited speaker, Workshop on Compound Semiconductor Materials and Devices (WOCSEMMAD), Palms Springs, CA, Feb. 2020.
9. “High Aspect Ratio β -Ga₂O₃ Nanostructures: MacEtch, Passivation, and Devices,” keynote speaker, International Conference on Nanoscience and Nanotechnology (ICONN), Brisbane, Australia, Feb. 2020.
10. “Nanostructured Semiconductor Materials & Devices: Innovation for Applications in Electronics, Photonics, and Quantum Technology,” invited seminar, Taiwan Semiconductor Manufacturing Company (TSMC), Hsinchu, Taiwan, Dec. 2019.
11. “Nanostructured Semiconductor Materials & Devices: Innovation for Applications in Electronics, Photonics, and Quantum Technology,” invited seminar, Taiwan National University, Taipei, Taiwan, Dec. 2019.
12. “Not Your Ordinary Etching: Metal-Assisted Chemical Etch (MacEtch) for III-V and Wide Bandgap Semiconductors,” invited speaker, 236th Electrochemical Society (ECS) Meeting, Atlanta, Oct. 2019.
13. “Nanostructured Semiconductor Materials and Devices: Innovation for Applications in Electronics, Photonics, and Quantum Technology,” Univ. Of Buffalo, invited Seminar Speaker, Sept. 2019.
14. “Not Your Ordinary Etching: MacEtch of Si, Ge, III-V, and Wide Bandgap Semiconductors,” Keynote speaker, The 19th IEEE International Conference on Nanotechnology, Macau, China, July 2019.
15. “MOCVD Selective Area Epitaxy of III-V Nanowire Arrays for Optoelectronic Applications,” invited speaker, Tsinghua International Nano-Optoelectronics Workshop (iNOW), Guangzhou and Shenzhen, China, July 2019.
16. “Semiconductor Nanowires: Past, Present, and Speculations about future directions,” invited speaker, 13th International NanoScience Student Conference (INASCON), Tsinghua Univ. Beijing, China, July 2019.
17. “Extreme Miniaturization of Passive Electronic Components by Self-Rolled-up Membrane (S-RuM) Nanotechnology,” Annual ShanghaiTech Symposium on Information and Technology, Plenary talk, Shanghai, China, June 2019.
18. “Nanostructured Semiconductor Materials & Devices: Innovation for Applications in Electronics, Photonics, and Quantum Technology,” Univ. Texas, Austin, invited seminar speaker, June 2019.
19. “Not Your Ordinary Etching: Metal-assisted chemical etching of Si, Ge, III-V and Wide Bandgap Semiconductors,” invited speaker, Zhejiang University, China, invited seminar, March 2019.
20. “>50:1 Aspect Ratio β -Ga₂O₃ Fin Array by MacEtch and Towards FinFETs,” invited speaker, Workshop on Compound Semiconductor Materials and Devices (WOCSEMMAD), Jacksonville, FL, invited talk, Feb. 2019.
21. “Not Your Ordinary Etching: Metal-assisted chemical etching for III-V and Wide Bandgap Semiconductors,” Silicon Carbide (SiC) Materials & Devices Workshop, AFRL, invited speaker, Dec. 2018.
22. “Enabling Device Miniaturization and Integration by Self-Rolled-up Membrane (S-RuM) Nanotechnology,” Plenary speaker, International Nanomembrane Origami Workshop, Fudan Univ. Shanghai, China, Nov. 2018.
23. “Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications,” Univ. of Washington, Seattle, Department of Materials Science, invited seminar speaker, Oct. 2018.

24. "Nanostructured Semiconductor Materials and Devices," Pacific Northwest National Laboratory, invited speaker, Oct. 2018.
25. "Not your ordinary etching: MacEtch for III-V Compound Semiconductors," IEEE Research and Applications of Photonics in Defense Conference (RAPID), Florida, invited speaker, Aug. 2018.
26. "S-RuM nanotechnology: a monolithic 3D MEMS Platform for Extreme Passive Electronics Miniaturization, IOT Sensing, and Biomedical Applications," Jiangsu Institute of Technology, Nanjing, China, invited speaker, Aug. 2018.
27. "Surface Engineering via Inverse-MacEtch for photodetectors: from Ge to Ga₂O₃," International Nano-Optoelectronics Workshop (iNOW), Berkeley, CA, invited speaker, July 2018.
28. "Semiconductor Materials & Devices: Innovation for Electronic, Photonic, and Biomedical Applications," IBM, Albany, NY, invited speaker, July 2018.
29. "Semiconductor Materials & Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Pacific Northwest National Laboratory, invited speaker, June 2018.
30. "Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Univ. of California, Los Angeles, Department of Electrical and Computer Engineering, invited seminar speaker, May 7th, 2018.
31. "Novel Processing Technologies for 3D Architectures," Micron Inc., Boise, Idaho, invited talk, May 3rd, 2018.
32. "Continuously Tunable Uniaxial Strain Engineering in Graphene via Self-Rolled-Up Membrane Technology," MRS, Symposium NM11, Phoenix, AZ, invited talk, April 2018.
33. "Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Zhejiang University, China, invited seminar speaker, March 2018.
34. "Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Univ. of California, San Diego, Nano Engineering department, invited seminar speaker, Feb. 2018.
35. "MacEtch Enabled New Device Concepts from InGaAs to Ga₂O₃," WOCSEMMAD, San Diego, CA, invited talk, Feb. 2018.
36. "Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Univ. of Texas, Austin, MRC Distinguished Lecture Series at the Microelectronics Research Center, Feb. 2018.
37. "Not Your Ordinary Etching: MacEtch for Semiconductor Nanostructures and Devices," IEEE Photonics Conference, Florida, Oct. 2017, short course.
38. "III-V Nanowire Arrays: Selective Lateral and Vertical Epitaxy and Devices," IUMRS-ICAM, Kyoto, Japan, Aug. 2017, invited talk.
39. "Metal-assisted chemical Etch (MacEtch) for High Aspect Ratio Semiconductor Nanostructures and Device Applications," International Nano-Optoelectronics Workshop (iNOW), Tianjin/Qian'an, China, Aug. 2017, invited talk.
40. "Publishing in Applied Physics Letters and in General," Fudan Univ. and SIMIT, China, June 2017, invited lectures.
41. "Semiconductor Nanomaterials and Devices Innovation for Electronic and Photonic Applications," Shanghai Institute of Technical Physics of the Chinese Academy of Sciences, China, June 2017, invited seminar.
42. "In-plane Nanowire Transistors," Compound Semiconductor Week, Berlin, May 2017, invited talk.
43. "Semiconductor Nanomaterials and Devices Innovation for Electronic and Photonic Applications," Ecole Polytechnique de Montreal, April 2017, invited seminar speaker.

44. "Extreme Miniaturization of Passive Electronic Devices by Self-Rolled-up Membrane Nanotechnology," China Semiconductor Technology International Conference (CSTIC 2017), Shanghai, China, Mar. 2017, invited speaker.
45. "Semiconductor Nanomaterials and Devices Innovation for Electronic and Photonic Applications," Stanford University, Feb. 2017, invited seminar speaker.
46. "Semiconductor Nanomaterials and Devices: Innovation for Electronic, Photonic, and Biomedical Applications," Vanderbilt University, Feb. 2017, invited seminar speaker.
47. "Semiconductor Processing: Wet Etch, Dry Etch, and now MacEtch," Conference on Optoelectronic and Microelectronic Materials and Devices (COMMAD), Sydney, Australia, Dec. 2016, invited speaker.
48. "Miniaturization of Passive Electronic Devices by Self-Rolled-Up Membrane Nanotechnology," Materials Research Society (MRS) Fall Meeting, Boston, Dec. 2016, invited speaker.
49. "Heterogeneous Integration III-V Nanowires on Si and van der Waals Substrates," Materials Research Society (MRS) Fall Meeting, Boston, Dec. 2016, invited speaker.
50. "III-V Nanowires and Nano fins: Growth, Etching, and Devices," International Conference on Solid State Devices and Materials (SSDM), Tsukuba, Japan, Sept. 2016, invited speaker.
51. "Growth and hierarchical assembly of nanomaterials," CIMTEC 2016 - 7th Forum on New Materials, Perugia, Italy, June 2016, invited speaker.
52. "Nanowire and Nanomembrane Electronics: performance and scalability," Naval Research Laboratory, May 2016, invited seminar speaker.
53. "Nanowire and nanomembrane electronics: performance and scalability," Vanderbilt University, Mar. 2016, invited speaker of VINCE Colloquium.
54. "Selective Lateral Epitaxy of III-V Nanowires for Scalable and High Performance Electronics," PacificChem, Hawaii, Dec. 2015, invited speaker.
55. "III-V Nanowire-based 3D Electronics Enabled by Selective Lateral Epitaxy," Mater. Research Soc. Boston, Nov. 2015, invited speaker.
56. "Nanowire and Nanomembrane Electronics," IEEE PKU Chapter, Beijing, China, Nov. 2015, IEEENano distinguished lecture.
57. "III-V nanowire growth and high speed electronics," NTT International Symposium on Nanoscale Transport and Technology, Atsugi, Japan, Nov. 2015, invited speaker.
58. "Self-rolled-up membrane for passive electronics," AVS Topical, Shanghai, Oct. 2015, keynote speaker.
59. "Planar III-V nanowire for high speed electronics," AVS Topical, Shanghai, Oct. 2015, invited speaker.
60. "III-V nanowires and 2D van der Waals sheets: interfaces and devices," SPIE, San Diego, Aug. 2015, invited speaker.
61. "Nanowire and Nanomembrane Electronics: Performance and Scalability," Airforce Research Laboratory, Dayton, Ohio, June 2015, invited speaker.
62. "Metal-Assisted Chemical Etching (MacEtch): Wet etch, Dry etch, and now MacEtch," Symposium OO, Materials Research Society Meeting, San Francisco, April, 2015, tutorial speaker.
63. "Microtube arrays create cozy space for neurons to grow ... fast," Army Research Office, Workshop on Compound Semiconductors for Biological Applications, Mar. 2015, invited speaker.
64. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," Arizona State University, Mar. 2015, invited seminar speaker.
65. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," Naval Research Laboratory, Feb. 2015, invited colloquium speaker.

66. "Semiconductor nanostructures for solar water splitting." Hydrogen Production & Fuel Cells Division Symposium, I2CNER, Kyushu, Japan, Feb. 5th, 2015.
67. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," Carnegie Mellon, Nov. 2014, invited seminar.
68. "3D hierarchical architectures based on self-rolled-up membranes," SES Annual Technical Meeting, Eringen Medal Symposium, Purdue Univ., Oct. 2014, Keynote speaker.
69. "RF Performance of 3D III-V Nanowire T-Gate HEMTs Grown by VLS Method," Nanowire Growth Workshop, Eindhoven, Netherlands, Aug. 2014, invited speaker.
70. "III-V Nanowire Solar Cells on Graphene via Direct van der Waals Epitaxy," IEEE Summer Topicals Meeting Series: Nanowire Materials and Integrated Photonics (NWIP), Montreal, Canada, July 2014, invited speaker.
71. "Metal-Assisted Chemical Etching (MacEtch): Wet etch, Dry etch, and now MacEtch," ScanDisk, July 2014, invited talk.
72. "Metal-Assisted Chemical Etching (MacEtch): Wet etch, Dry etch, and now MacEtch," Lam Research, June 2014, invited talk.
73. "Au-Free III-V Nanowire Epitaxy Capabilities and Related Devices," Lam Research, June 2014, invited talk.
74. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," Norwegian University of Science and Technology, May 2014, invited seminar.
75. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," The 6th International Symposium on Bioanalysis, Biomedical Engineering and Nanotechnology (ISBBN 2014), Hunan, China, May 2014, invited speaker.
76. "Nanowire Solar Cells on Graphene by Direct Epitaxy," University of Puerto Rico, Rio Piedras, March 2014, invited seminar.
77. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance and scalability," Nano and Giga Challenges in Electronics, Photonics and Renewable Energy (NGC2014), Tempe, Arizona, March 2014, invited talk.
78. "III-V nanowire arrays for solar hydrogen production," International Conference on Hydrogen Generation, Kyushu, Japan, Jan. 2014, invited talk.
79. "Nanowire Electronics," ISDRS, Washington D.C., Dec. 2013, invited talk.
80. "Nanowire Photovoltaics," Asia Communications & Photonics Conference (ACP), Beijing, Nov. 2013, invited talk.
81. "Semiconductor Nanoelectronic and Nanophotonic Devices: Performance and Scalability," IEEE IEEE Southeastern Michigan Chapter IV and University of Michigan, Nov. 2013, invited lecture.
82. "Van der Waals nanoepitaxy: mechanism and applications," SPIE Optics and Photonics (Nano Epitaxy), San Diego, Aug. 2013, invited talk.
83. "Semiconductor nanowire array based electronic and photovoltaic devices: formation, performance, and scalability," IEEE Nano, Beijing, Aug. 2013, invited tutorial.
84. "Self-rolled-up micro and nanotubes for photonics and passive electronics," IEEE Nano, Beijing, Aug. 2013, invited tutorial.
85. "Metal-assisted chemical etching (MacEtch): anisotropic wet etching for optoelectronics," International Conference on Nanophotonics (ICNP)/Conference on Advanced Optoelectronics and Micro/Nano Optics (AOM), Hong Kong, May 2013, invited talk.
86. "Semiconductor nanowire array: from formation to applications," International Conference on Nanophotonics (ICNP)/Conference on Advanced Optoelectronics and Micro/Nano Optics (AOM), Hong Kong, May 2013, invited tutorial.
87. "Semiconductor Nanotechnology: controllability and scalability," University of Texas, Arlington, April 2013, invited speaker.

88. "Semiconductor Nanoelectronic and Nanophotonic Devices: controllability and scalability," Case Western Research University, March 2013, invited speaker.
89. "Semiconductor Nanoelectronic and Nanophotonic Devices: towards controllability and manufacturability," International Conference on Nano Science and Nano Technology, Gwangju, Korea, Nov. 2012, plenary speaker.
90. "Nanowire based solar cells," SPIE/COS Photonics Asia, Beijing, Nov. 2012, invited talk.
91. "Semiconductor Nanoelectronic and Nanophotonic Devices: towards controllability and manufacturability," Integrative NanoScience Institute, Florida State University, Oct. 2012, invited speaker.
92. "Semiconductor Nanoelectronic and Nanophotonic Devices: performance, controllability and manufacturability," Solid State Technology and Devices seminar series, University of California, Berkeley, Oct. 2012, invited speaker.
93. "Ordered array of III-V nanowire growth by MOCVD and applications in nanoelectronics," SPIE Optics and Photonics Meeting, "Nanoepitaxy: Materials and Devices IV," San Diego, 12-16 August, 2012, invited speaker.
94. "III-V Nanowires on Si for Monolithic Tandem Solar Cells," Photonics North Conference, Montreal, Canada, June 2012; invited speaker.
95. "Array Based Nanotechnology Inspired Bio and Energy Applications," NanoCEMMS industry advisory board meeting, UIUC, March 2012; invited speaker.
96. "Rolled-up Tube based Nanophotonics," American Physics Society (APS) March Meeting, Boston, March 2012; invited speaker.
97. "Semiconductor Nanoelectronic and Nanophotonic Devices: towards controllability and manufacturability," Princeton University, Dec. 7th, 2011, invited seminar speaker.
98. "Semiconductor Nanoelectronic and Nanophotonic Devices: towards Controllability and Manufacturability," Lehigh University, Sept. 2nd, 2011, invited seminar speaker.
99. "Semiconductor Nanoelectronic and Nanophotonic Devices: towards Controllability and Manufacturability," The 11th Annual Emerging Information and Technology Conference (EITC), July 2011, University of Chicago, invited talk.
100. "III-V Nanowire FETs," DARPA COSMOS Program PI Meeting, June 2011, Long Beach, CA, invited guest speaker.
101. "III-V Nanoelectronic and Nanophotonic Devices: Towards Controllability and Manufacturability," Nano Photonics and Electronics Industry Affiliates Program, Illinois, May 2011, invited talk.
102. "III-V and Silicon Based Nanoelectronic and Nanophotonic Devices: Towards Controllability and Manufacturability," Center for Quantum Devices Seminar, Northwestern University, March 21, 2011, invited speaker.
103. "III-V Nanoelectronic and Nanophotonic Devices: towards controllability and manufacturability" Applied Physics and Electrical Engineering Joint Colloquium, Harvard University, February 2011, invited speaker.
104. "Planar GaAs Nanowires on GaAs (100) substrates: self-aligned, twin-defect free and transfer-printable," NSF-MEXT (Japan) Young Researchers Exchange Program, Dec. 2010, Kyoto, Japan, invited talk.
105. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," University of Southern California, Nov. 2010, invited seminar speaker.
106. "Ordered Silicon Nanowire Array Based Solar Cells Produced by Metal Assisted Chemical Etching," Photonic Society annual meeting, Denver, Nov. 2010.
107. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Rensselaer Polytechnic Institute, Oct. 2010, invited seminar speaker.

108. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Ohio State University, Oct 2010, invited seminar speaker.
109. "Microtube resonators: strain-induced and large area assembled," NSF-MEXT (Japan) Young Researchers Exchange Program, Oct. 2010, UIUC, invited talk.
110. "III-V Planar Nanowire FETs and Rolled-up Tube Based Nanophotonics," Nano-EP seminar series, Sept. 2010, UIUC, invited talk.
111. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Beijing Institute of Technology, July 2010, invited talk.
112. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Tsing Hua University, China, July 2010, invited talk.
113. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Stanford University, May 2010, invited talk.
114. "Towards High Performance III-V Semiconductor Nanowire-FETs and Nano-lasers," Solid State Seminar (S3) Series, April 2010, Norte Dame, invited speaker.
115. "Bottom-up and Manufacturable: New Paradigms in III-V Semiconductor Nanostructures and Device Prospects," NSF Nano-CEMMS Annual Industry Meeting, March 2010, invited talk.
116. "Strain-induced Self-rolling III-V Tubular nanostructure: Formation process and Photonic Application," SPIE Photonic West, San Francisco, Jan 2010, invited talk.
117. "Compound semiconductor 1D nanostructures and Device Prospects," Purdue University, Birk Nanotechnology Center, November 2009, invited talk.
118. "Compound Semiconductor Nanotubes and Nanowires for photonic and electronic Applications," Argonne National Laboratory, Aug. 2009, invited talk.
119. "Compound Semiconductor Nanotubes and Nanowires for photonic and electronic Applications," Department of Microelectronics, Peking University, China, July 2009, invited talk.
120. "Compound Semiconductor Nanotubes and Nanowires for Nanophotonic and Nanoelectronic Applications," Institute for Advanced Materials, Devices, and Nanotechnology (IAMDN), Rutgers University, April 2009, invited talk.
121. "Compound Semiconductor Nanotubes and Nanowires for photonic and electronic Applications," 1st NSF-sponsored US-Argentina Workshop on Nanomaterials, Bariloche, Argentina, March 2009, invited talk.
122. "3D IC and Semiconductor Nanotechnology," IBM, February 2009, invited talk.
123. "Compound semiconductor nanotubes and nanowires," University of California, Los Angeles, February 2009, invited talk.
124. "Compound semiconductor nanotubes and nanowires," Texas Tech University, February 2009, invited talk.
125. "Nanotechnology: compound semiconductor nanotubes and nanowires," ECE 200 seminar, University of Illinois, February 2009.
126. "Nanotechnology: compound semiconductor nanotubes and nanowires," Beckman Institute Nanohour Series, University of Illinois, November 2008.
127. "III-V semiconductor nanotubes and nanowires," University of California, Los Angeles, February 2009, invited talk.
128. "III-V semiconductor nanotubes and planar nanowires," University of Wisconsin, Madison, 2008, invited seminar.
129. "What is your impact factor," NSF Nano-CEMMS graduate student meeting, UIUC, 2010, invited talk.

Contributed Talks and Posters (partial list):

1. "MacEtch Produces Damage-Free High-Aspect-Ratio Semiconductor Nanostructures—Current Status and Future Promise," EMC, virtual, June 2020, oral presentation.
2. "Large Area van der Waals Epitaxy of MoS₂ on III-Nitride Substrates," MRS, Phoenix, AZ, April 2017, Oral Presentation.
3. Kyoocho Jung, Cheyu Liu, JD Kim, Wonsik Choi, Weidong Zhou, Hao-Chung Kuo, and Xiuling Li, "Large Area MoS₂ van der Waals Epitaxy on III-Ns and the Epitaxial Formation of a n-MoS₂/p-InGaN Diode," IEEE Photonics Conference, Kona, Hawaii, Oct. 2016, oral presentation.
4. Wen Huang, Moyang Li, Songbin Gong and Xiuling Li, "RFIC transformer with 12x size reduction and 15x performance enhancement by self-rolled-up membrane nanotechnology," InterPACKICNMM, San Francisco, July 2015, poster.
5. Yi Song, Parsian K. Mohseni, Seung Hyun Kim, Jae Cheol Shin, Chen Zhang, Kelson Chabak, and Xiuling Li, "InP FinFETs with Damage-Free and Record High-Aspect-Ratio (45:1) Fins Fabricated by Metal-Assisted Chemical Etching," 73th Device Research Conference, Columbus, Ohio, 2015, oral presentation.
6. Wen Huang, Moyang Li, Songbin Gong and Xiuling Li, "Self-rolled-up Tube Transformers: Extreme Miniaturization and Performance Enhancement," 73th Device Research Conference, Columbus, Ohio, 2015, oral presentation.
7. Paul Froeter, Yu Huang, Olivia V. Cangellaris, Wen Huang, Martha U. Gillette, Justin Williams and Xiuling Li, "Superior Neuronal Outgrowth Guidance and Rate Enhancement using Silicon Nitride Self-Rolled-up Membranes," 73th Device Research Conference, Columbus, Ohio, 2015, poster presentation.
8. Wonsik Choi, Parsian K. Mohseni, Eric Seabron, William L. Wilson, and Xiuling Li, "Doping Dynamics of Selective Lateral Epitaxy-Grown p-n Junction GaAs Nanowires Revealed Using Scan-Probe Microscopy Techniques," 57th Electronic Materials Conference, Columbus, Ohio, 2015, oral presentation.
9. Wonsik Choi, Parsian K. Mohseni, Eric Seabron, William L. Wilson, and Xiuling Li
10. X. Yu, Ehsan Arbabi, Lynford Goddard, Xiuling Li, and Xiaogang Chen, "Monolithically integrated self-rolled-up tube based vertical coupler with planar waveguide – a new 3D photonic integration scheme," Advanced Photonics, Boston, July 2015, oral presentation.
11. Christopher A. Edwards, Steven J. McKeown, SukWon Hwang, Paul Froeter, Xiuling Li, John A. Rogers, Gabriel Popescu, Lynford L. Goddard, "In-situ measurements of nanoscale phenomena using diffraction phase microscopy," SPIE, San Francisco, CA, Feb. 2015, oral presentation.
12. "RF Performance of 3D III-V Nanowire T-Gate HEMTs Grown by VLS Method," Kelson D., Xin Miao, Chen Zhang, Dennis E. Walker Jr., Xiuling Li, Device Research Conference (DRC), 72nd Annual, Santa Barbara, CA, June 2014, oral presentation.
13. "III-As Pillar Arrays by Metal-Assisted Chemical Etching for Photonic Applications," Parsian Mohseni, Pan Lei, Xiang Zhao, Seung Hyun Kim, Karthik Balasundaram, Jeong Dong Kim, James J. Coleman, and Xiuling Li, CLEO, San Jose, June 2013, oral presentation.
14. "Twinning Superlattice in VLS Grown Planar GaAs Nanowires Induced by Impurity Doping," X. Li, IEEE Photonics Conference, San Francisco, Sept. 2012, oral presentation.
15. "Wafer-Scale InGaAs Nanowire Epitaxy on Si and Graphene Substrates," Parsian Mohseni, Jae Cheol Shin, Xiuling Li, International Symposium on Compound Semiconductors, Santa Barbara, CA, Aug. 2012, oral presentation.
16. "Anisotropic Metal-Assisted-Chemical-Etching for III-V Compound Semiconductors," Parsian Mohseni, Karthik Balasundaram, Matt DeJarld, Jae Cheol Shin, Debashis Chanda, John A. Rogers, and Xiuling Li, International Symposium on Compound Semiconductors, Santa Barbara, CA, Aug. 2012, poster.

17. "Ultrathin InAs Nanowire Growth by Au-assisted TMI_n Dissociation," K. Jung and X. Li, Electronic Materials Conference, Penn State, June 2012, oral presentation.
18. "Semiconductor Nano-Materials and Devices: towards manufacturability," X. Li, NSF Workshop on Micro, Nano, and Bio Systems, Washington DC, March 2012, invited participant, poster.
19. "Planar VLS Grown GaAs Nanowire Array Based HEMTs," X. Miao and X. Li, International Semiconductor Device Research Symposium (ISDRS), College Park, Maryland, Dec. 2011, oral presentation.
20. "Monolithically Grown In_xGa_{1-x}As Nanowire Array on Silicon Tandem Solar Cells with High Efficiency," J. C. Shin and X. Li, IEEE Photonics Conference, Arlington, Oct. 2011, oral presentation.
21. "Characteristics of In_xGa_{1-x}As Nanowires Heteroepitaxially Grown on Silicon," J. C. Shin and X. Li, ACCGE-18 and OMVPE-15 workshop, Aug 2011, oral presentation.
22. "Monolithically Grown AlGaAs-GaAs Planar Nanowire High Electron Mobility Transistor," X. Miao and X. Li, ACCGE-18 and OMVPE-15 workshop, Aug 2011, poster.
23. "Ternary In_xGa_{1-x}As Nanowires on Silicon Substrates: 1D Heterogeneous Epitaxy, Bandgap Engineering, and Photovoltaics," J. C. Shin and X. Li, EMC, June 2011, oral presentation.
24. "Towards Planar GaAs Nanowire Array High Electron Mobility Transistor," X. Miao and X. Li, DRC, June 2011, poster.
25. "Monolithically Grown In_xGa_{1-x}As Nanowire on Silicon Tandem Solar Cells with High Efficiency," J. C. Shin, K. J. Ho, J.A. Rogers, and X. Li, DRC, June 2011, poster.
26. "Strain-Induced Self-rolling of Semiconductor Membranes: Effect of Geometry, Energetics, and Kinetics," I. Chun, A. Challa, B. Derickson, K. J. Hsia, and X. Li, CLEO, May 2011, oral presentation.
27. "AlGaAs/GaAs nanowire HEMT: planar GaAs nanowire and AlGaAs shell interface study," X. Miao and X. Li, MRS, April 2011, oral presentation.
28. "Silicon Microwire Array Based Solar Cells Produced by Metal-Assisted Chemical Etching," UGIM, June 2010, Purdue University, oral presentation.
29. "Planar <110> GaAs Nanowires: Effects of Impurity Doping and Substrate Orientation," UGIM, June 2010, Purdue University, poster.
30. "Twinning Superlattice in VLS Grown <110> Planar GaAs Nanowires Induced by Impurity Doping," EMC, June 2010, Norte Dame, oral presentation.
31. "Non-lithographic Patterning and Metal-Assisted Chemical Etching for Manufacturing of Tunable Light-Emitting Silicon Nanowire Arrays," CLEO/QELS, May 2010, San Jose, oral presentation.
32. "Geometry Dependence of the Strain-driven Self-rolling of Semiconductor Nanotubes," Ik Su Chun, Huan Li, Archana Challa, K Jimmy Hsia, Xiuling Li, TMS annual meeting, 2010, oral presentation.
33. "Planar III-V nanowires," International Conference on 1-dimensional Nanomaterials (ICON), Xiuling Li, oral presentation, Dec. 2009.
34. "Strain Driven Self-rolling III-V Tubes," International Conference on 1-dimensional Nanomaterials (ICON), Xiuling Li et. al., poster presentation, Dec. 2009.
35. "Metal-assisted chemical etching for manufacturing of Si nanowire arrays," International Conference on 1-dimensional Nanomaterials (ICON), Xiuling Li et al., poster presentation, Dec. 2009.
36. "High-Mobility Planar GaAs Nanowires on (100) Substrates for Nanoelectronic Applications," Organometallic Vapor Phase Epitaxy (OMVPE) Workshop, Wisconsin, August 2009, oral presentation.
37. "Ultra-thin-walled III-Arsenide Microtubes with Embedded QW Light Emitters," ACCGE-17, Wisconsin, August, 2009, oral presentation.
38. "GaAs <110> Nanowires: Planar, Self-aligned, Twin-free, High-mobility and Transfer-Printable," 14th OptoElectronics and Communications Conference, Hong Kong, July 2009, oral presentation.

39. "GaAs FET with a High Mobility Self-Assembled Planar Nanowire Channel on a (100) Substrate," IEEE Device Research Conference, Penn State, June 2009, oral presentation.
40. 'Ultra-Thin-Walled III-Arsenide Microtubes with Embedded QW Light Emitters: Room Temperature PL Characteristics," CLEO, Baltimore, May 2009, postdeadline oral presentation.
41. "III-V micro and nanotubes: from formation process to photonic applications," Semiconductor Laser Workshop, Baltimore, May 2009, oral presentation and session organizer.
42. "Self-aligned <110> planar GaAs nanowires for low twin density and high integratability," MRS 2008 symposium LL, poster presentation.
43. "Metal-semiconductor hybrid micro- and nanotubes: large area assembly, dispersion, functionalization and characterization," MRS 2008 Symposium JJ, oral presentation (recognized as Outstanding Symposium Paper).
44. "Three Dimensional Nanoscale Pattern Formation in Light-emitting Porous Silicon," MRS 2008 Symposium MM, oral presentation.
45. "MOCVD Grown III-V Nanowires: In-plane, Self-aligned and Transfer-printable," IEEE LEOS 2008 annual meeting, oral presentation (best student paper award finalist).
46. "Self-Aligned Planar GaAs Nanowires Grown by MOCVD on GaAs (100) Substrates," CLEO/QELS 2008, oral presentation.
47. "3-D Nanoscale Pattern Formation in Porous Silicon," CLEO/QELS 2008, oral presentation.
48. "Engineered large area fabrication of ordered InGaAs-GaAs nanotube arrays," MRS 2007, oral presentation.

TEACHING

At University of Texas, Austin,

TBD

At University of Illinois,

ECE 444 IC Device Theory and Fabrication, *course director*, <http://fabweb.ece.illinois.edu>

ECE 518 Adv. Semiconductor Nanotechnology

ECE 590G Graduate Seminar in Special Topics

TE 401 Develop Breakthrough Projects

SERVICE

Offices held in Professional Societies

1. IEEE Andrew Grove Award Committee (2021-2022)
2. IEEE Nanotechnology Council Fellow Search Committee (2021 -)
3. IEEE Electron Device Society, Fellow evaluation committee (2021 -)
4. IEEE Photonics Society, VP of Finance and Administration (2018-2020)
5. IEEE Photonics Society, Nominations and Appointment (N&A) committee (2016-)
6. IEEE Photonics Society, Board of governors (2014-2016)
7. IEEE Photonic Society representative to IEEE Nanotechnology Council (2010-2014)

Conference organizing committees served in Professional Societies

1. Materials Research Society (MRS) meetings (2020, 2021)
2. The 18th International Conference on Metal Organic Vapor Phase Epitaxy (ICMOVPE-XVIII), organizing committee (2016-2018)

3. Device Research Conference, Technical Program Committee (2015-2018)
4. Organizer of the inaugural symposium on Metal-assisted chemical Etching (MacEtch) at MRS (spring 2015)
5. Organizer of the first workshop on Self-Rolled-Up Membrane (S-RUM) Technology (2014)
6. Center for Nano Science and Technology (CNST) Annual Workshop, co-chair (2013)
7. 9th ACM/IEEE Symposium on Nanoscale Architecture (NANOARCH), TPC (2013)
8. IEEE Summer Topical: Micro and Nanocavity Integrated Photonics, organizing committee (2013)
9. CLEO Science & Innovation committee 6: Optical Materials, Fabrication & Characterization (2012, 2013)
10. Electronic Materials Conference (EMC) organizing committee (2012, 2013)
11. Asia Communications & Photonics Conference (ACP) Technical Program Subcommittee 6: energy (2012, 2013)
12. International Semiconductor Device Research Symposium (ISDRS) Program Committee (2011)
13. Symposium organizer for 220th Electrochemical Society Meeting (2011)
14. Device Research Conference (DRC) session chair (2010)
15. Electronic Materials Conference (EMC) session chair (2010)
16. IEEE University Government Industry Micro/Nano Symposium Technical Committee (2010)
17. IEEE Photonic Society *Semiconductor Laser workshop session organizer* (09)
18. IEEE Photonic Society *Semiconductor Laser Technical Committee* (08, 09, 10)

Editorships of Journals

1. Deputy Editor, Applied Physics Letters (2015 -)
2. Associate Editor, IEEE Transaction on Nanotechnology (2015)
3. Associate Editor, Scientific Reports, Nature Publishing Group (2014 -2015)
4. Journal of Electronic Materials, Guest Editor for a special issue (2013)
5. Associate Editor, IEEE Photonics Journal (2012 - 2015)

Reviewer

1. National Science Foundation
2. Army Research Office
3. Department of Energy
4. IEEE, Nature, PNAS, NPG, AAAS, ACS, AIPP, APS, IOP etc. journals

University/campus service

1. Campus: Illinois International Advisory Committee (2018-); Office of Technology Management Advisory Committee (2019-); Beckman Institute Coordinating Committee (2020-).
2. Grainger College of Engineering: Advisory Committee on Named Faculty Appointments (Chair-Elect, 2020-2022), Executive Committee (2016-2019); Curriculum Committee for the new degree of BS in Innovation, Leadership, and Engineering Entrepreneurship (ILEE); Safety Advisory Committee; Search Committees for ECE department head (2014, 2019), MNTL director (2014), and ARI associate director (2014).
3. Electrical and Computer Engineering Committees: Promotion & Tenure Review (Chair, 2019, 2020, 2021), Curriculum, Colloquium, Public Relations, Graduate Admissions, Graduate Recruitment, Fellowship, and Faculty Search committees.
4. Center for Nanotechnology Science and Technology (CNST): Annual Workshop Organizing Committee, co-Chair, poster judge (2008-).

5. Nick Holonyak Jr. Micro and Nanotechnology Lab (MNTL): Interim Director (2020-), Executive Committee (2008-), Cleanroom Operations & Safety Committee (2012-).
6. COE, VCR, and Graduate College internal proposal review panels

Outreach activities

1. Invited speaker, My Nonlinear Career Trajectory, Women Chemists Committee, Chemistry, UIUC (May 2020).
2. Principle Investigator for NSF RET site on Nanotechnology (2014-2017)
3. Faculty Advisor, IEEE Electron Device Society Student Chapter (2010-)
4. Faculty advisor, Nano Electronics and Photonics (nanoEP) Seminar Series (2011-)
5. Instructor, Girls Adventures in Math, Engineering, and Science summer camp (2010-)
6. Speaker, NSF Nano-CEMMS graduate student meeting on industry research (2010)
7. Delegate, NSF US/Japan Young Scientists Exchange Team on Nanomanufacturing (2010)
8. Faculty host, NSF REU students (2009-)
9. Plenary speaker, Evening with Industry for ECE Eta Kappa Nu Honor Society (2007)
10. Panel member, career day at Barkstall Elementary school, Champaign, IL (2007)

SUPERVISION OF GRADUATE STUDENTS AND POSTDOCTORAL RESEARCHERS

Current Group Members:

Graduate students (10):

Hsien-Chih Huang, Julian Michaels, Zhengdong Yang, Apratim Khandelwal, Shunya Namiki, Clarence Chan, Lukas Janavicius, Zhongjie Ren, Xihang Wu, Kristen Nguyen

Undergraduate researchers (3):

Siyuan Wang, Bill Jiang, Jarad Prill

Former Thesis Students (year of graduation, current affiliation):

Ph.D. degrees (18): Ik Su Chun (2011, Intel), Ryan Dowdy (2013, Northrup Grumman), Xin Miao (2014, IBM), Ki Jun Yu (2014, UIUC), Kevin Bassett (2015, Honeywell), Chen Zhang (2015, IBM), Karthik Balasundaram (2015, Fenwick & West LLP), Kyoocho Jung (2016, Samsung), Kelson Chabak (2016, Air Force Research Lab), Yi Song (2016, IBM), Jeong Dong Kim (2017, Intel), Wen Huang (2017, UIUC), Eric Seabron (2017, Northrup Grumman), Kuo-Ju Chen (2017, TSMC), Paul Froeter (2017, UIUC), Lingyu Kong (2017, Micron), Che-yu Liu (2018, TSMC), Wonsik Choi (2019, Samsung).

M.S. degrees (terminal): Xi Zeng (2008), Archana Challa (2010, Intel), Seth Fortuna (2010, Berkeley, Sandia National Lab), Xiang Zhao (2015, OminiVision), SeungHyun (Ryan) Kim (Korea, military service), Moyang Li (2018, Apple).

B.S. degrees with thesis: Serena Liou (2009, Northrup Grumman), Winston Chern (2010, MIT), Hsing-Keng Tsai (2010), Mathew DeJarld (2011, Michigan), Mohammad Malik (2011, Microsoft), Michael Liu (2011, UIUC), Paul Froeter (2012, UIUC), SeungHyun (Ryan) Kim (2012, UIUC), Aiyin Liu (2012, UIUC), Moyang Li (2013, UIUC), and Jeong Bin Lim (2014, UIUC), Yixuan (Nancy) Zhao, Clarence Chan (2016, UMichigan), Mantian Xue (2016, MIT), Siyu Liu (2019, Stanford), Qingyi Wang (2019, UIUC), Bill Jiang (2021, Amazon)

Former Postdoctoral Researchers (years of training, current affiliation)

Jae Cheol Shin (2010-2011, Professor, Yeungnam Univ. Korea), Xin Yu (2011-2014, TSMC), Parsian Mohseni (2012-2015, Associate Professor, Rochester Institute of Technology), Munho Kim (2016-2018, Assistant Professor, Nanyang Technology University, Singapore), and Wen Huang (2017-2018, Professor, Hefei Institute of Technology)

